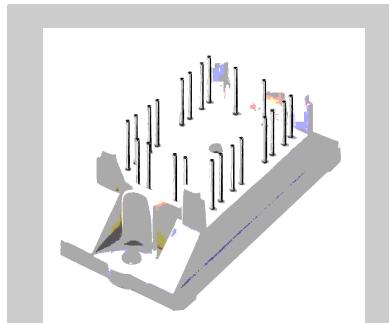


Features

- Trench Field Stop Technology IGBT adopted
- Low Saturation Voltage
- Positive Temperature Coefficient
- Fast switching
- Free Wheeling Diodes with fast and soft reverse recovery
- Industrial Standard Package with insulated substrate
- Temperature Sensor included

Preliminary Data

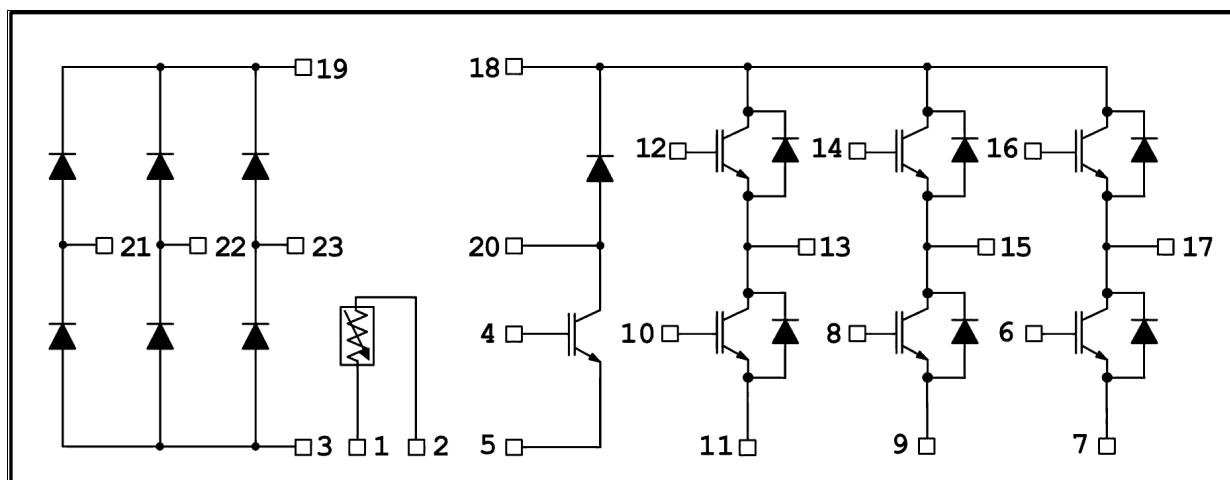

SISPM0

66.0 x 32.6 x 21.6mm

Applications

- Input from single or three phase grid
- Three Phase synchronous or asynchronous motor
- Dynamic Braking Operation

Internal Equivalent Circuit



Pin Description

Pin Number	Pin Name	Pin Description
1, 2	TH1, TH2	NTC-, NTC+
3	N	Negative DC Link Input
4	GB	Gate Input for Braking IGBT
5	EB	Emitter Input for Braking IGBT
6, 8, 10	GWN, GVN, GUN	Gate Input for Low-side W Phase, V Phase, U Phase
7, 9, 11	WN, VN, UN	Negative DC Link output W Phase, V Phase, U Phase
12, 14, 16	GUP, GVP, GWP	Gate Input for High-side U Phase, V Phase, W Phase
13, 15, 17	U, V, W	Output for U Phase, V Phase, W Phase
18	DCP	Positive DC Link Output
19	P	Positive DC Link Input
20	B	Output for Braking
21, 22, 23	R, S, T	Input for R Phase, S Phase, T Phase

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Item	Symbol	Parameter	Conditions	Value	Units
Input Rectifier	V_{RPM}	Repetitive Peak Reverse Voltage		1600	V
	I_{FAV}	Forward Current per Diode	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	10	A
	I_{FSM}	Surge Forward Current (Chip level)	@ $T_j = 150^\circ\text{C}$, $t_p = 10\text{ ms}$, half sine wave	200	A
	I^2t	I^2t - Value (Chip level)	@ $T_j = 150^\circ\text{C}$, $t_p = 10\text{ ms}$, half sine wave	200	A^2s
	P_D	Maximum Power Dissipation	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	50	W
	T_j	Operating Junction Temperature *(1)	-	-40 ~ 125	$^\circ\text{C}$
Transistor Inverter	V_{CES}	Collector-Emitter Breakdown Voltage	-	1200	V
	V_{GES}	Gate-Emitter Peak Voltage	-	± 20	V
	I_C	DC Collector Current	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	10	A
	I_{cpulse}	Repetitive Peak Collector Current	@ $t_p = 1\text{ ms}$	20	A
	P_D	Maximum Power Dissipation	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	60	W
	T_{SC}	SC Withstand Time (Chip level)	@ $V_{GE} = 15\text{ V}$, $V_{CE} = 600\text{ V}$	10	μs
Diode Inverter	T_j	Operating Junction Temperature *(2)	-	-40 ~ 125	$^\circ\text{C}$
	V_{RRM}	Repetitive Peak Reverse Voltage	-	1200	V
	I_F	DC Forward Current	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	10	A
	I_{FRM}	Repetitive Peak Forward Current	@ $t_p = 1\text{ ms}$	20	A
	P_D	Maximum Power Dissipation	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	30	W
Transistor Brake	T_j	Operating Junction Temperature *(1)	-	-40 ~ 125	$^\circ\text{C}$
	V_{CES}	Collector-Emitter Breakdown Voltage	-	1200	V
	V_{GES}	Gate-Emitter Peak Voltage	-	± 20	V
	I_C	DC Collector Current	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	8	A
	I_{cpulse}	Repetitive Peak Collector Current	@ $t_p = 1\text{ ms}$	16	A
	P_D	Maximum Power Dissipation	@ $T_j = 175^\circ\text{C}$, $T_C = 80^\circ\text{C}$	50	W
Diode Brake	T_{SC}	SC Withstand Time (Chip level)	@ $V_{GE} = 15\text{ V}$, $V_{CE} = 600\text{ V}$	10	μs
	T_j	Operating Junction Temperature *(2)	-	-40 ~ 125	$^\circ\text{C}$
	V_{RRM}	Repetitive Peak Reverse Voltage	-	1200	V
	I_F	DC Forward Current	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	8	A
	I_{FRM}	Repetitive Peak Forward Current	@ $t_p = 1\text{ ms}$	16	A
Module	P_D	Maximum Power Dissipation	@ $T_j = 150^\circ\text{C}$, $T_C = 80^\circ\text{C}$	30	W
	T_{stg}	Storage Temperature	-	-40 ~ 125	$^\circ\text{C}$
	V_{iso}	Isolation Voltage	@ AC 1minute	2500	V
	W	Weight	-	30	g

(Note *1) The Maximum junction temperature of chip is 150°C .
 (Note *2) The Maximum junction temperature of chip is 175°C .

LFC10G1207

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Input Rectifier Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_F	Diode Forward Voltage	$T_C = 25^\circ\text{C}, I_F = 10\text{ A}$	-	0.9	-	V
		$T_C = 125^\circ\text{C}, I_F = 10\text{ A}$	-	1.0	-	V
V_{to}	Threshold Voltage (Chip level)	$T_C = 125^\circ\text{C}$	-	0.83	-	V
I_R	Reverse Current (Chip level)	$T_C = 25^\circ\text{C}, V_{RRM} = 600\text{ V}$	-	0.05	-	mA
r_t	Slope Resistance (Chip level)	$T_C = 125^\circ\text{C}$	-	20.8	-	$\text{m}\Omega$
$R_{th(J-C)}$	Thermal Resistance (DIODE Part)	Junction-to-Case	-	1.4	-	$^\circ\text{C}/\text{W}$

Transistor-Inverter Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$V_{GE(th)}$	Gate-Emitter threshold Voltage	$V_{CE} = V_{GE}, I_{CE} = 10\text{ mA}$	-	6.4	-	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$T_C = 25^\circ\text{C}, I_{CE} = 10\text{ A}, V_{GE} = 15\text{ V}$	-	2.1	-	V
		$T_C = 125^\circ\text{C}, I_{CE} = 10\text{ A}, V_{GE} = 15\text{ V}$	-	2.5	-	V
I_{CES}	Collector-Emitter Cut-off Current	$V_{GE} = 0\text{ V}, V_{CE} = 1200\text{ V}$	-	-	1	mA
I_{GES}	Gate-Emitter Leakage Current	$V_{GE} = 20\text{ V}, V_{CE} = 0\text{ V}$	-	-	120	nA
C_{iss}	Input Capacitance	$V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}$ $f = 1\text{ MHz}, T_C = 25^\circ\text{C}$ (Chip level)	-	625	-	pF
C_{oss}	Output Capacitance		-	60	-	pF
C_{rss}	Reverse Transfer Capacitance		-	40	-	pF
$t_d(on)$	Turn-On Delay Time	$T_C = 125^\circ\text{C}, R_{G\ ON} = 32\ \Omega$ $R_{G\ OFF} = 32\ \Omega, L = 500\ \mu\text{H}$ $V_{CE} = 600\text{ V}, V_{GE} = -15\text{ V}\sim 15\text{ V}$ $I_{CE} = 10\text{ A}$	-	10	-	ns
t_r	Rise Time		-	28	-	ns
$t_d(off)$	Turn-Off Delay Time		-	270	-	ns
t_f	Fall Time		-	172	-	ns
E_{on}	Turn-On Switching Loss		-	1.1	-	mJ
E_{off}	Turn-Off Switching Loss		-	0.9	-	mJ
E_{ts}	Total Switching Loss		-	2.0	-	mJ
Q_G	Total Gate Charge		-	66	-	nC
Q_{GE}	Gate-Emitter Charge		-	19	-	nC
Q_{GC}	Gate-Collector Charge		-	28	-	nC
$R_{th(J-C)}$	Thermal Resistance (IGBT Part)	Junction-to-Case	-	1.4	-	$^\circ\text{C}/\text{W}$

Diode-Inverter Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units	
V_F	Diode Forward Voltage	$I_F = 10\text{ A}, V_{GE} = 0\text{ V}$	$T_C = 125^\circ\text{C}$	-	1.9	-	V
t_{rr}	Diode Reverse Recovery Time	$R_{G\ ON} = 32\ \Omega$	$T_C = 125^\circ\text{C}$	-	393	-	ns
I_{RRM}	Diode Peak Reverse Recovery Current	$L = 500\ \mu\text{H}$	$T_C = 125^\circ\text{C}$	-	16	-	A
Q_{rr}	Diode Reverse Recovery Charge	$V_{CE} = 1200\text{ V}$	$T_C = 125^\circ\text{C}$	-	2.2	-	μC
E_{rr}	Diode Reverse Recovery Energy	$V_{GE} = -15\text{ V}\sim 15\text{ V}$	$I_{CE} = 10\text{ A}$	$T_C = 125^\circ\text{C}$	-	0.9	mJ
$R_{th(J-C)}$	Thermal Resistance (DIODE Part)	Junction-to-Case	-	2.2	-	$^\circ\text{C}/\text{W}$	

Transistor- Brake Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$V_{GE(th)}$	Gate-Emitter threshold Voltage	$V_{CE} = V_{GE}, I_{CE} = 10\text{ mA}$	-	6.3	-	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$T_C = 25^\circ\text{C}, I_{CE} = 8\text{ A}, V_{GE} = 15\text{ V}$	-	2.0	-	V
		$T_C = 125^\circ\text{C}, I_{CE} = 8\text{ A}, V_{GE} = 15\text{ V}$	-	2.4	-	V
I_{CES}	Collector-Emitter Cut-off Current	$V_{GE} = 0\text{ V}, V_{CE} = 1200\text{ V}$	-	-	1	mA
I_{GES}	Gate-Emitter Leakage Current	$V_{GE} = 20\text{ V}, V_{CE} = 0\text{ V}$	-	-	120	nA
C_{iss}	Input Capacitance	$V_{CE} = 25\text{ V}, V_{GE} = 0\text{ V}$ $f = 1\text{ MHz}, T_C = 25^\circ\text{C}$ (Chip level)	-	490	-	pF
C_{oss}	Output Capacitance		-	50	-	pF
C_{rss}	Reverse Transfer Capacitance		-	30	-	pF

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$t_d(\text{on})$	Turn-On Delay Time	$T_C = 125^\circ\text{C}$, $R_{G\text{ ON}} = 32 \Omega$ $R_{G\text{ OFF}} = 32 \Omega$, $L = 500 \mu\text{H}$ $V_{CE} = 600 \text{ V}$, $V_{GE} = -15 \text{ V} \sim 15 \text{ V}$ $I_{CE} = 8 \text{ A}$	-	4.9	-	ns
t_r	Rise Time		-	24	-	ns
$t_d(\text{off})$	Turn-Off Delay Time		-	269	-	ns
t_f	Fall Time		-	210	-	ns
E_{on}	Turn-On Switching Loss		-	1.0	-	mJ
E_{off}	Turn-Off Switching Loss		-	0.9	-	mJ
E_{ts}	Total Switching Loss		-	1.9	-	mJ
Q_G	Total Gate Charge		-	56	-	nC
Q_{GE}	Gate-Emitter Charge		-	19	-	nC
Q_{GC}	Gate-Collector Charge		-	23	-	nC
$R_{\text{th}(J-C)}$	Thermal Resistance (IGBT Part)	Junction-to-Case		1.8	-	°C/W

Diode-Brake Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions		Min	Typ	Max	Units	
V_F	Diode Forward Voltage	$I_F = 8 \text{ A}$	$V_{GE} = 0 \text{ V}$	$T_C = 125^\circ\text{C}$	-	1.7	-	V
t_{rr}	Diode Reverse Recovery Time	$R_{G\text{ ON}} = 32 \Omega$ $L = 500 \mu\text{H}$ $V_{CE} = 600 \text{ V}$ $V_{GE} = -15 \text{ V} \sim 15 \text{ V}$ $I_{CE} = 8 \text{ A}$	$T_C = 125^\circ\text{C}$	-	607	-	ns	
I_{RRM}	Diode Peak Reverse Recovery Current		$T_C = 125^\circ\text{C}$	-	8	-	A	
Q_{rr}	Diode Reverse Recovery Charge		$T_C = 125^\circ\text{C}$	-	1.8	-	μC	
E_{rr}	Diode Reverse Recovery Energy		$T_C = 125^\circ\text{C}$	-	0.7	-	mJ	
$R_{\text{th}(J-C)}$	Thermal Resistance (DIODE Part)	Junction-to-Case		-	2.0	-	°C/W	

NTC thermistor Characteristics

Symbol	Parameter	Test Conditions		Min	Typ	Max	Units
R_{25}	Resistance	$T_C = 25^\circ\text{C}$		-	22	-	kΩ
P	Power	$T_C = 25^\circ\text{C}$		-	210	-	mW
$B_{25/100}$	B Constant	$T_C = 25^\circ\text{C}$, ± 3% tolerance		-	4000	-	K

* This specifications may not be considered as an assurance of characteristics and may not have same characteristics in case of using different test systems from @LSIS. We therefore strongly recommend prior consultation of our engineers.

LFC10G1207

Input Rectifier

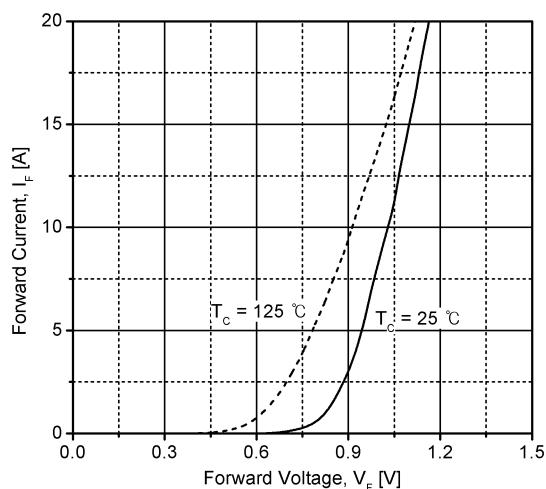


Fig 1. Typical Diode Forward Characteristics

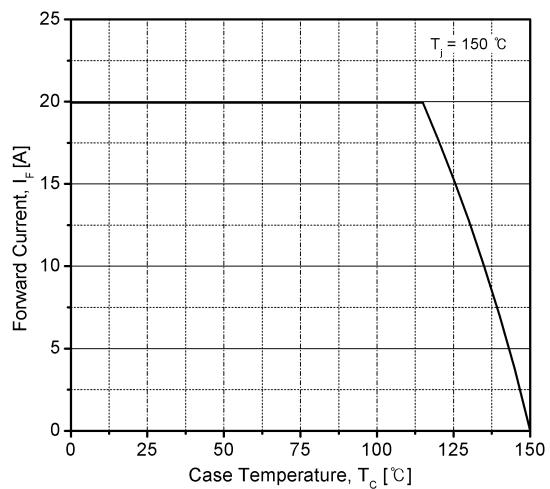


Fig 2. Case Temperature vs. Forward Current

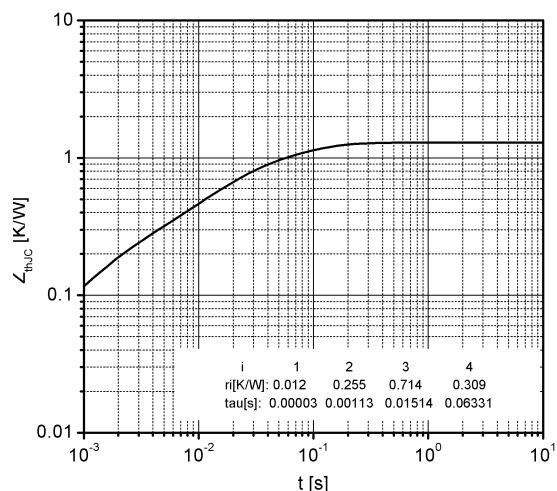


Fig 3. Typical Diode Thermal Impedance

Transistor-Inverter/Diode-Inverter

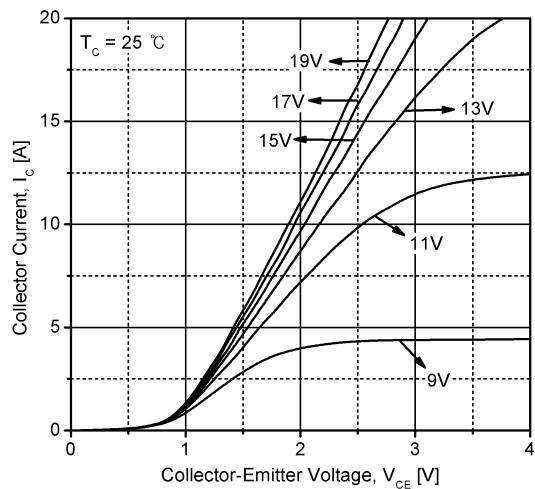


Fig 4. Typical IGBT Output Characteristics

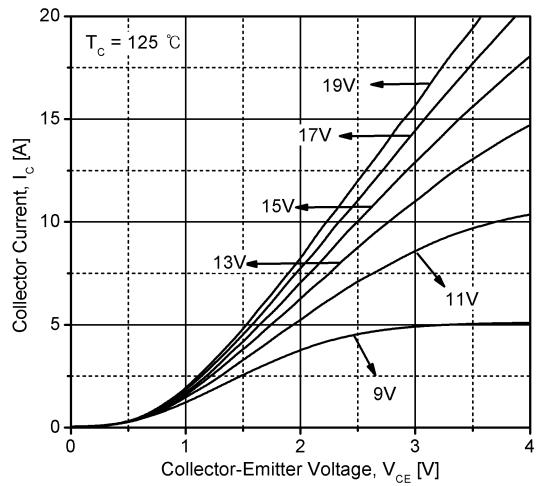


Fig 5. Typical IGBT Output Characteristics

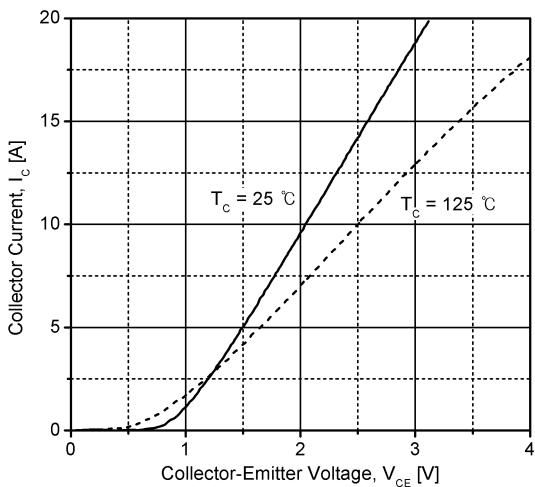


Fig 6. Typical IGBT Output Characteristics

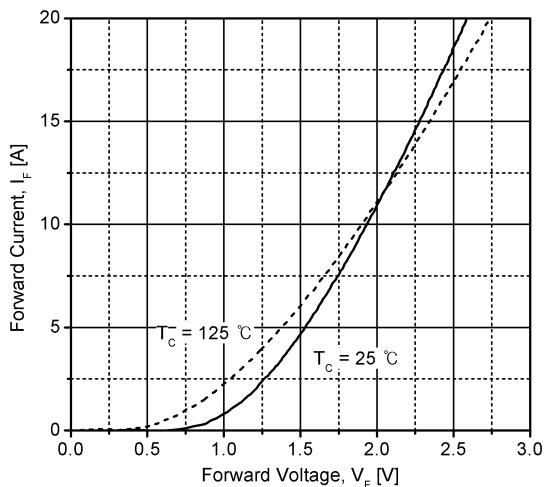


Fig 7. Typical Diode Forward Characteristics

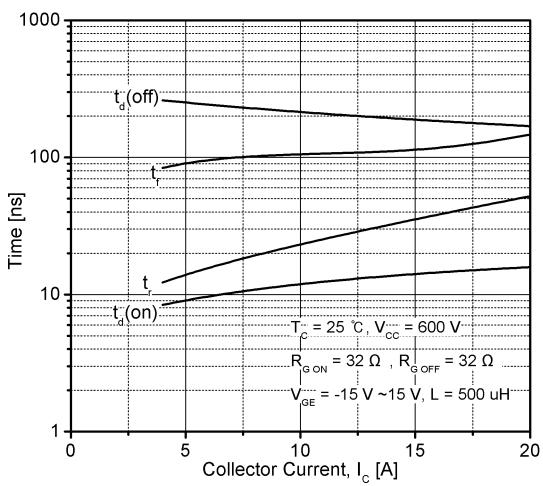


Fig 8. Typical Switching Time vs. Collector Current

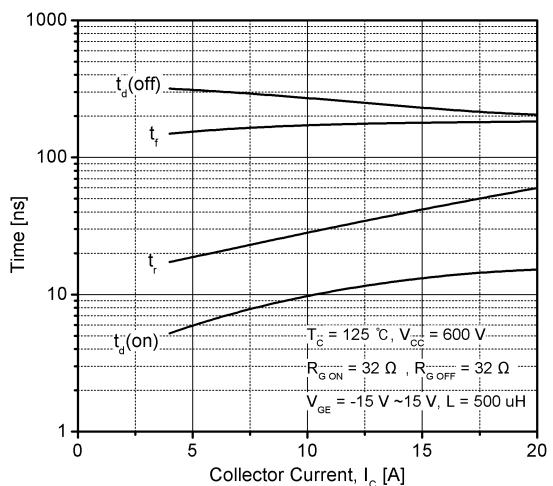
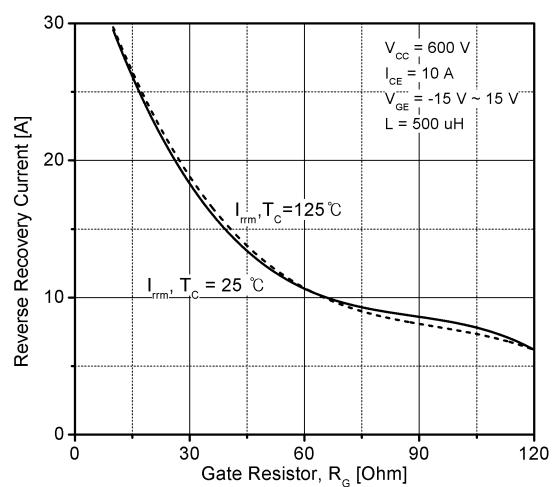
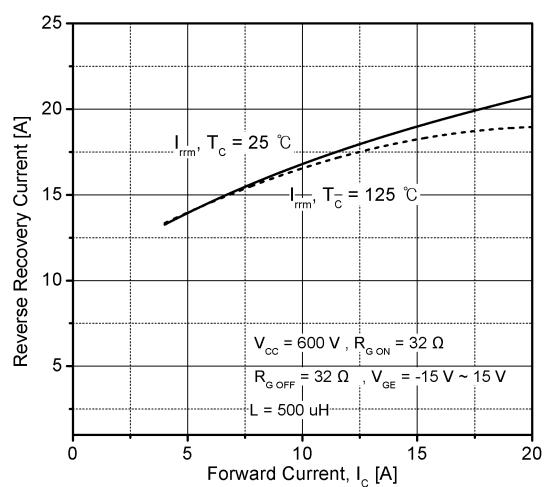
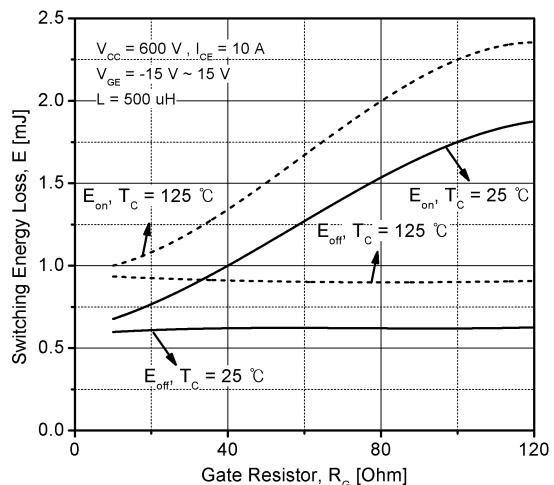
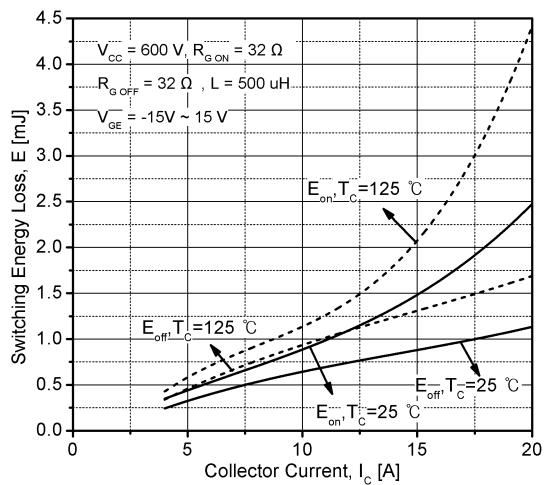
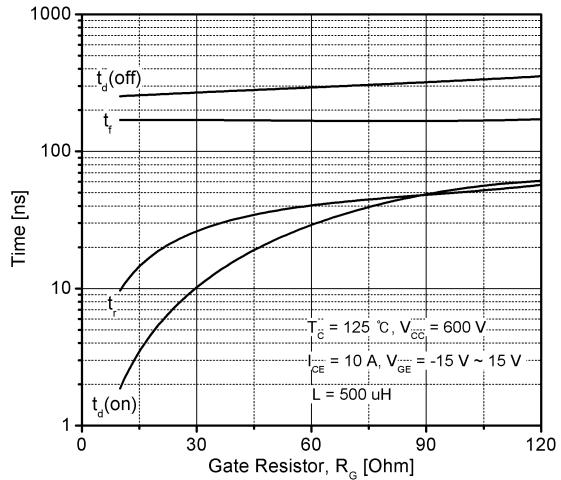
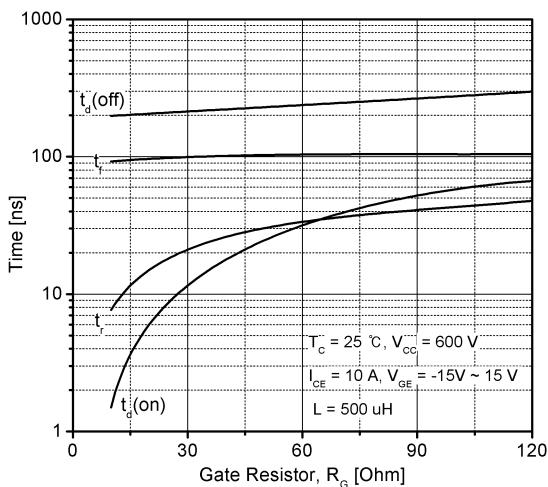
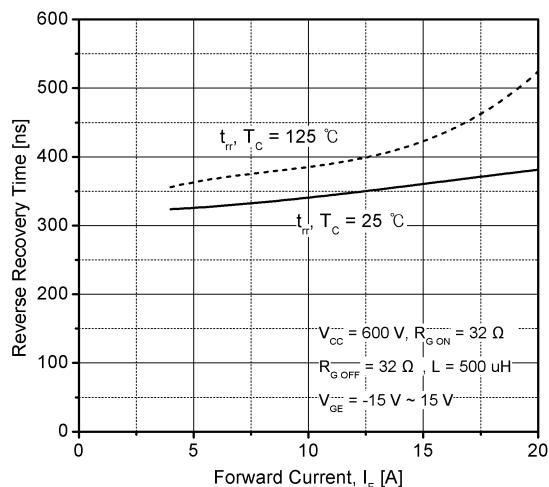
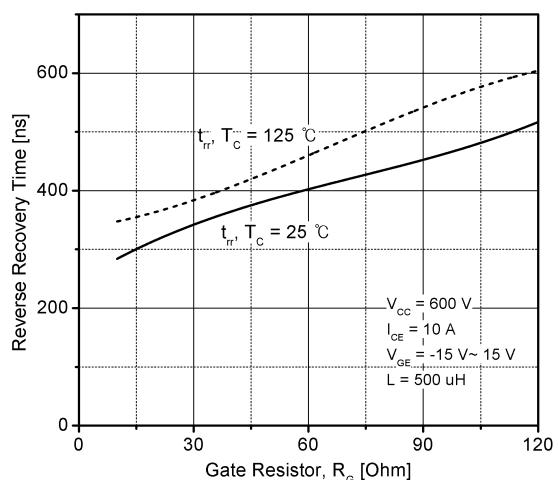
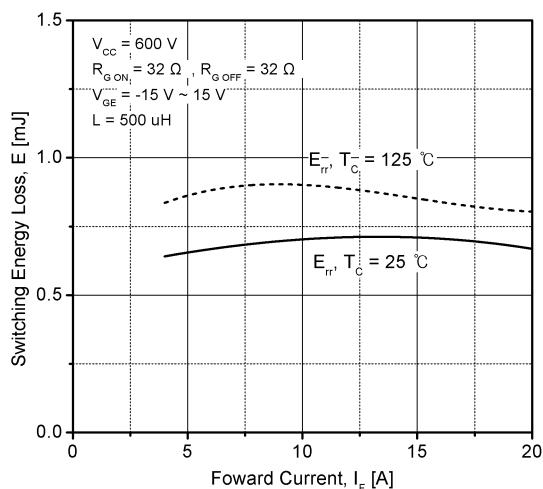
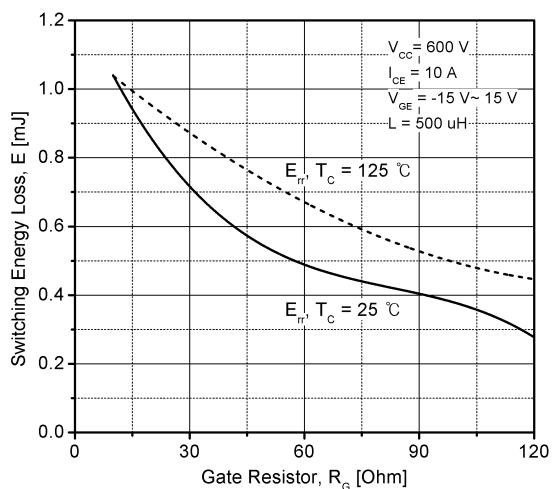
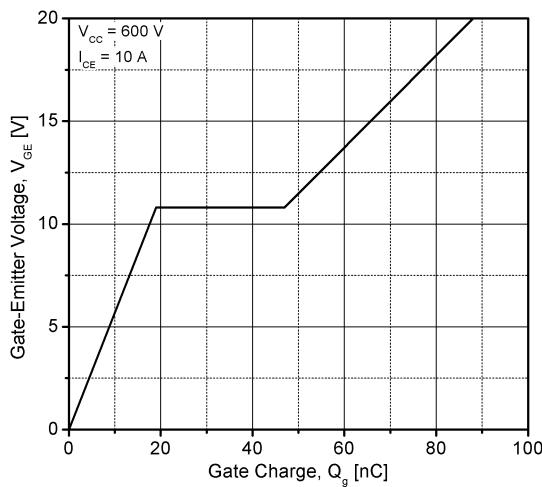
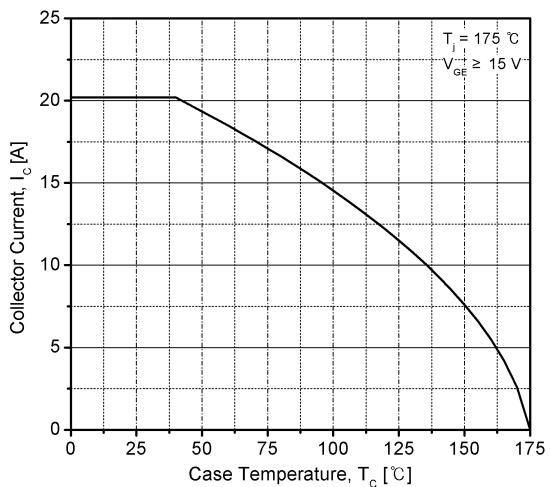


Fig 9. Typical Switching Time vs. Collector Current

LFC10G1207

Transistor-Inverter/Diode-Inverter



Transistor-Inverter/Diode-Inverter

Fig 16. Typical Recovery Characteristics of Diode

Fig 17. Typical Recovery Characteristics of Diode

Fig 18. Typical Diode Switching Loss

Fig 19. Typical Diode Switching Loss

Fig 20. Typical Gate Charge Characteristics

Fig 21. Case Temperature vs. Collector Current

LFC10G1207

Transistor-Inverter/Diode-Inverter

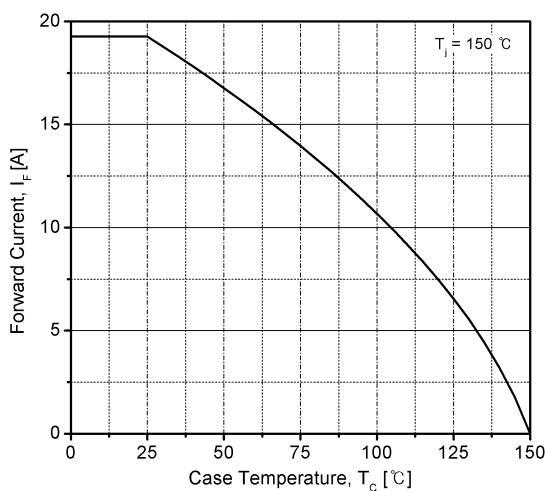


Fig 22. Case Temperature vs. Forward Current

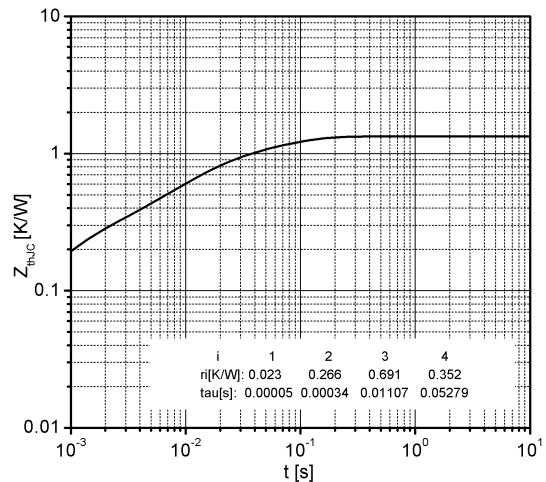


Fig 23. Typical IGBT Thermal Impedance

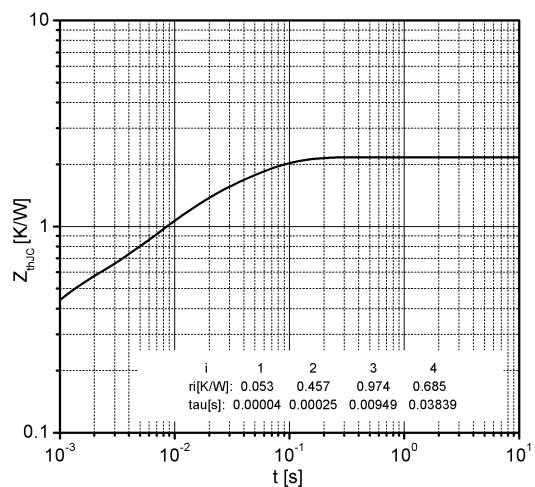


Fig 24. Typical Diode Thermal Impedance

Transistor-Brake/Diode-Brake

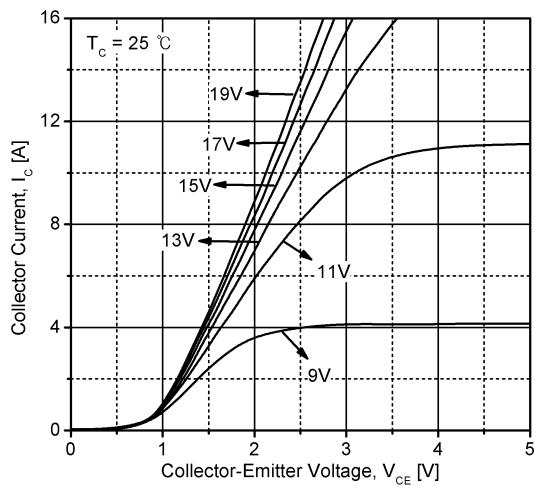


Fig 25. Typical IGBT Output Characteristics

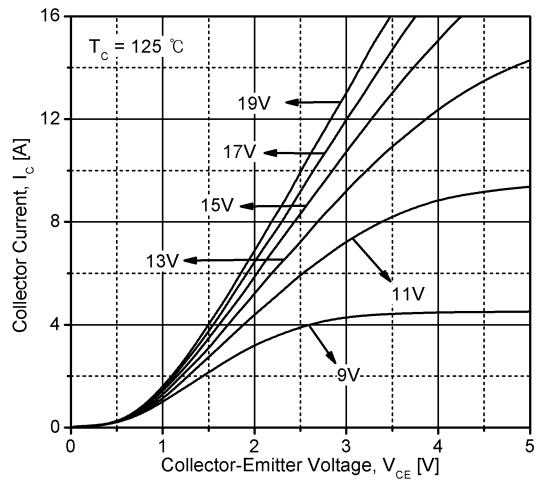


Fig 26. Typical IGBT Output Characteristics

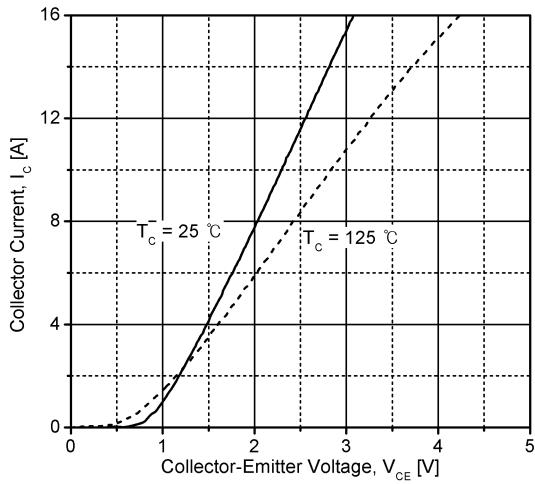


Fig 27. Typical IGBT Output Characteristics

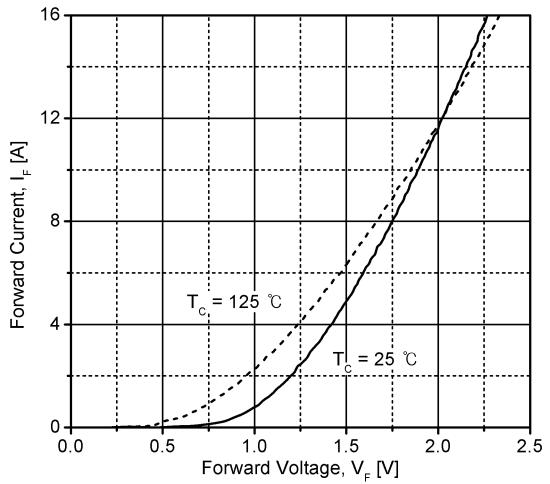


Fig 28. Typical Diode Forward Characteristics

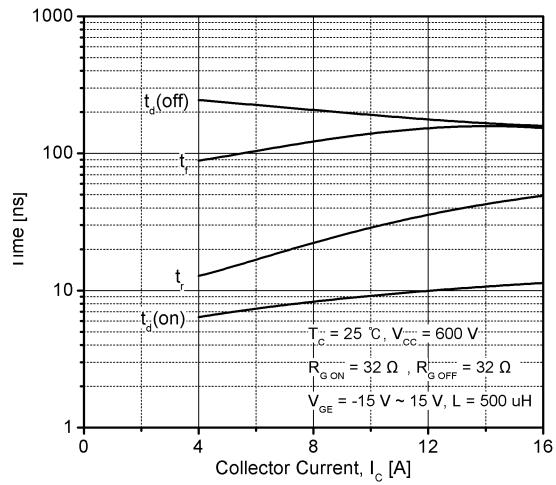


Fig 29. Typical Switching Time vs. Collector Current

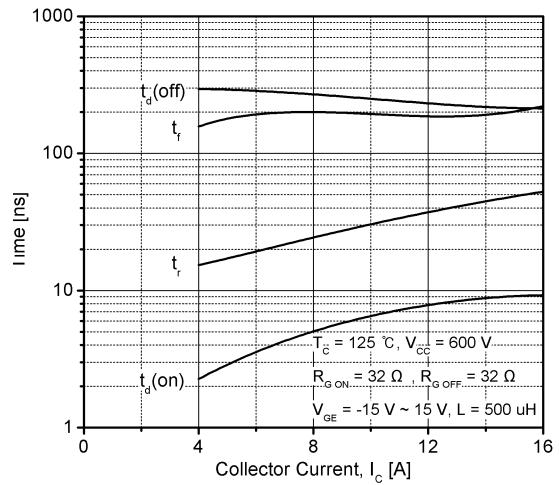


Fig 30. Typical Switching Time vs. Collector Current

LFC10G1207

Transistor-Brake/Diode-Brake

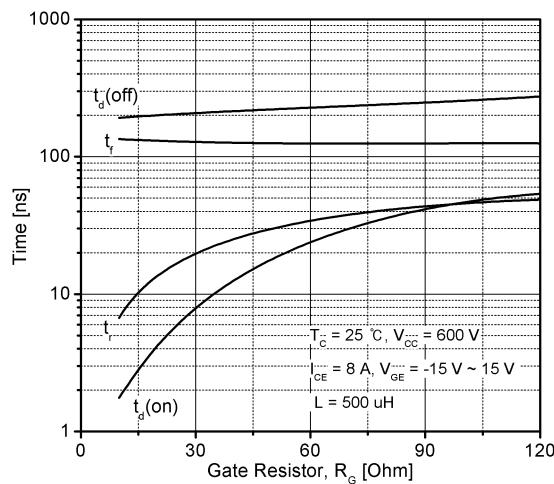


Fig 31. Typical Switching Time vs. Gate Resistor

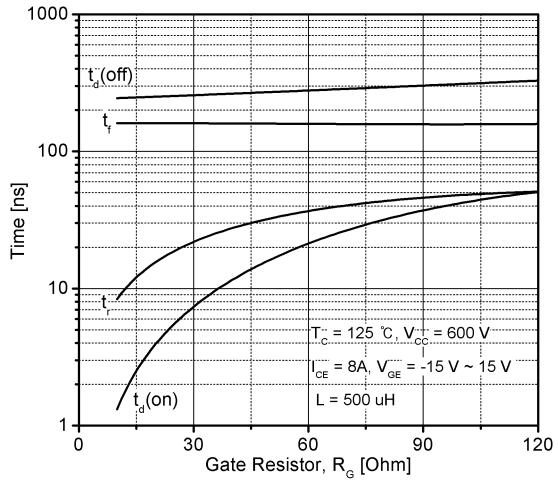


Fig 32. Typical Switching Time vs. Gate Resistor

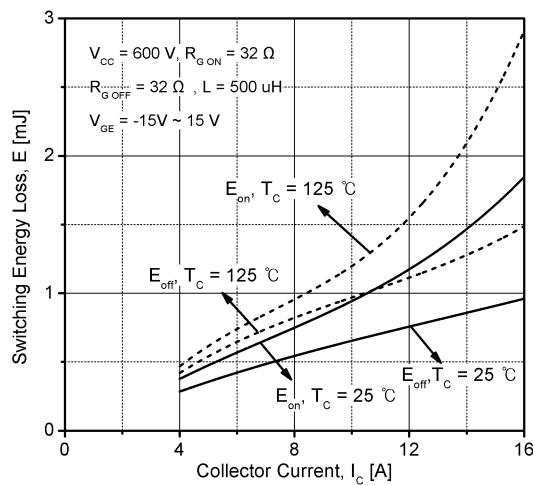


Fig 33. Typical IGBT Switching Loss

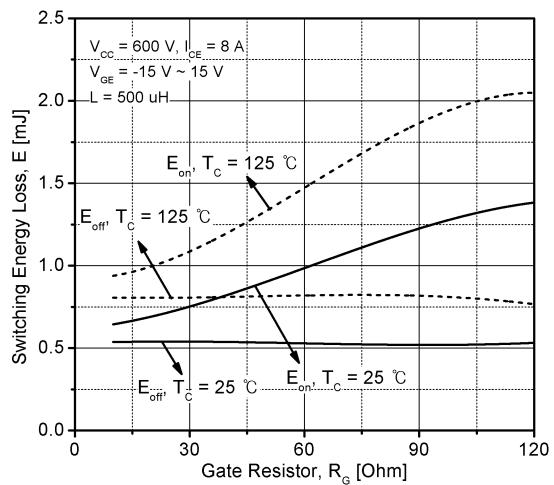


Fig 34. Typical IGBT Switching Loss

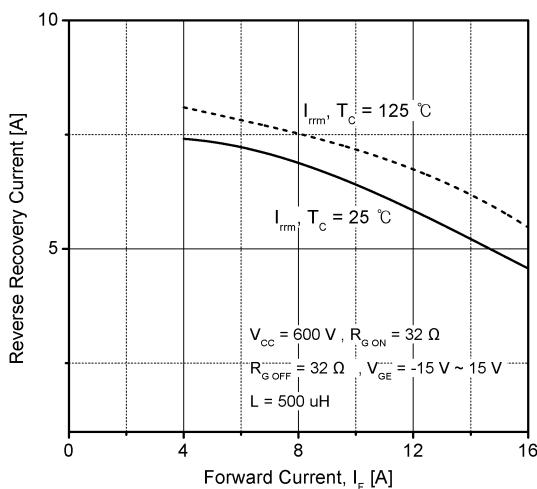


Fig 35. Typical Recovery Characteristics of Diode

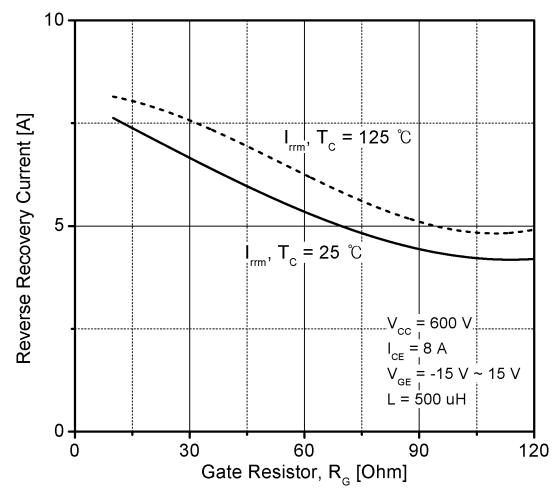


Fig 36. Typical Recovery Characteristics of Diode

Transistor-Brake/Diode-Brake

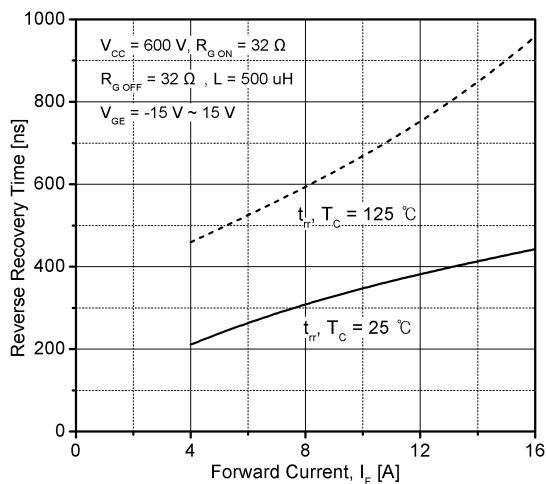


Fig 37. Typical Recovery Characteristics of Diode

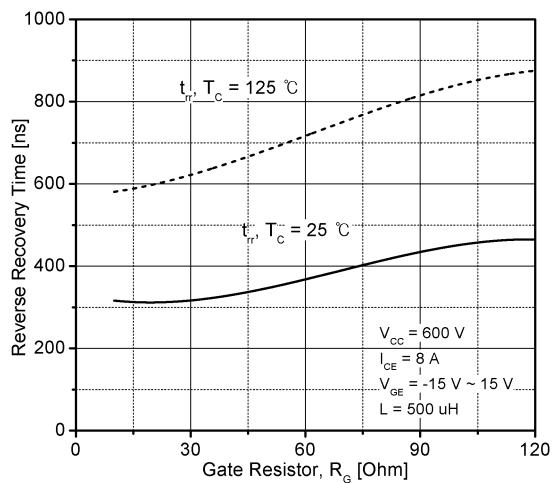


Fig 38. Typical Recovery Characteristics of Diode

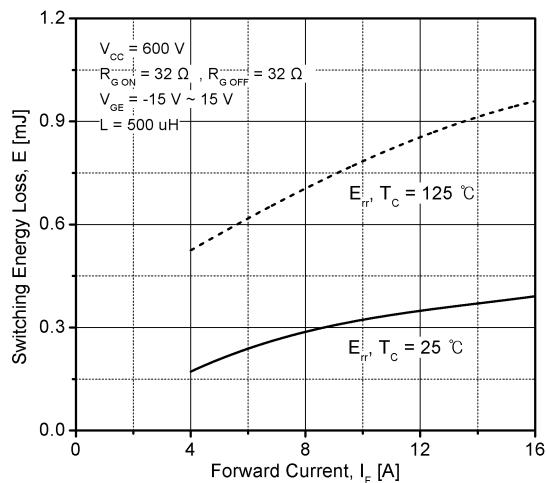


Fig 39. Typical Diode Switching Loss

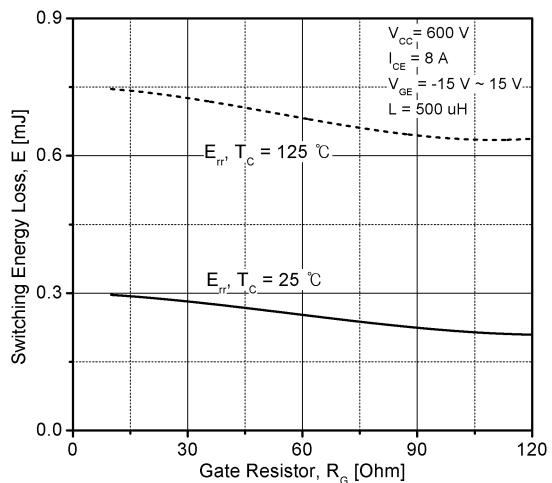


Fig 40. Typical Diode Switching Loss

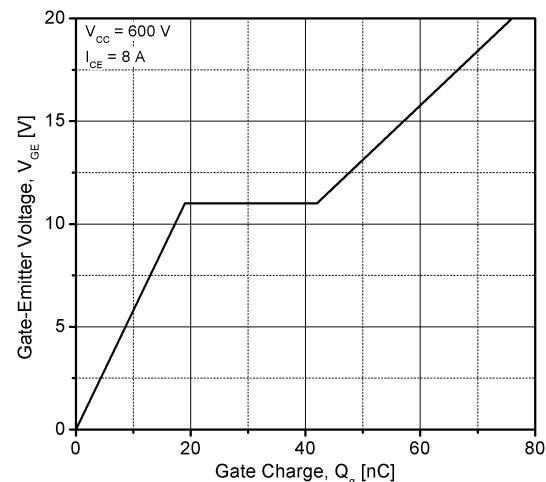


Fig 41. Typical Gate Charge Characteristics

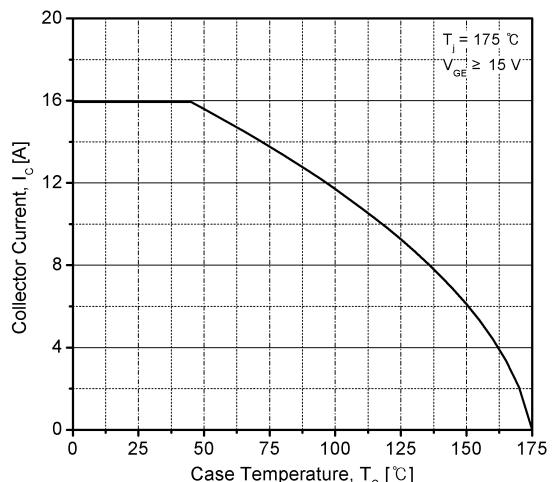


Fig 42. Case Temperature vs. Collector Current

LFC10G1207

Transistor-Brake/Diode-Brake

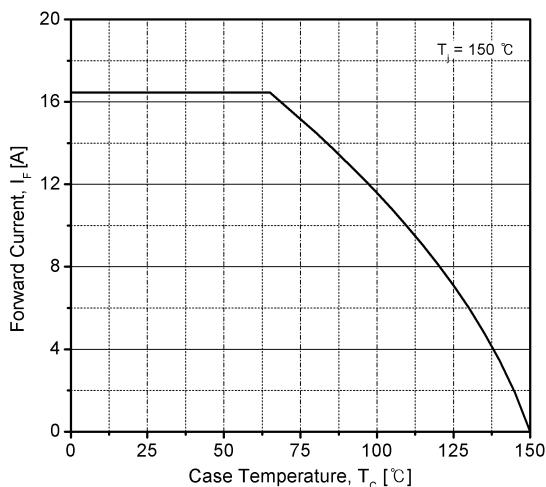


Fig 43. Case Temperature vs. Forward Current

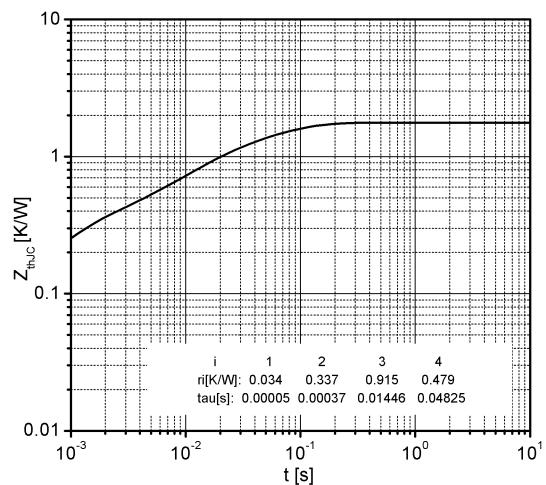


Fig 44. Typical IGBT Thermal Impedance

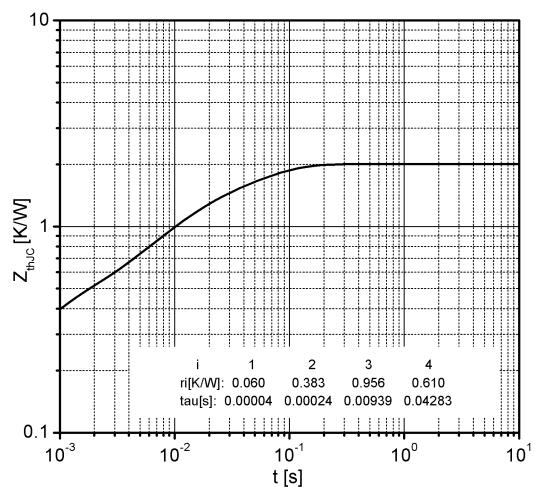


Fig 45. Typical Diode Thermal Impedance

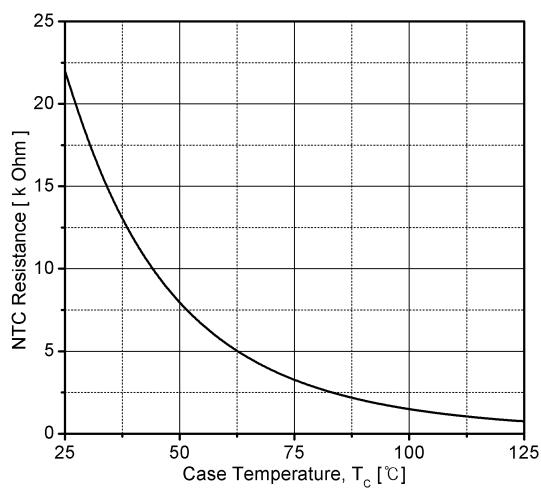
NTC

Fig 46. Typical NTC Characteristics

LFC10G1207

Package Dimension(Dimension in mm)

